# Formal Methods and Tools in Railways

Recent Successes and Future Challenges

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Istituto di Scienza e Tecnologie dell'Informazione "A. Faedo"

#### $\mathsf{BCS}/\mathsf{FACS}/\mathsf{FME}$ seminar, London, UK, March $26^{\mathrm{th}}$ , 2025

#### Outline



- Railway research
  - Sustainable mobility
  - Industrial standards
- Formal methods and tools
  - Formal methods in railways
  - Formal tools in railways
- Recent successes
  - Rail scheduling
  - Railway signalling
  - Smart railways
- Future challenges
- Concluding remarks

# Railway research

# - **FM**

#### EU green deal initiative



→ H2020 Shift2Rail Joint Undertaking (JU): €920 million (2014–2020)

*"formal methods are fundamental for safe and reliable technological advances to increase the competitiveness of the European rail industry "* 

→ Horizon Europe Europe's Rail JU:  $\in$ 1.2 billion (2020–2027)

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China State Key Laboratory of Rail Traffic Control and Safety

## FMT involved in EU, national and regional projects





 $\rightarrow$  Learning formal models (e.g., digital twins) for predictive maintenance

**ASECURail** Formal methods and CSIRT for the railway sector (H2020, 2019–21)

 $\rightarrow$  Demonstrator to evaluate cost, benefits and required learning curve of using formal methods for rigorous specification of a railway signalling infrastructure

SAtellite-based Signalling and Automation SysTems on Railways along with Formal Method and Moving Block Validation (H2020, 2017–19)

 $\rightarrow$  Requirements analysis plus safety, hazard and performance analyses of moving block signalling scenarios with the most suitable formal methods and tools

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SmaRIERS: Smart Railway Infrastructures: Efficiency, Reliability and Safety (RT, 2021–23)

STINGRAY: SmarT station INtelliGent RAilwaY (RT, 2018–21)

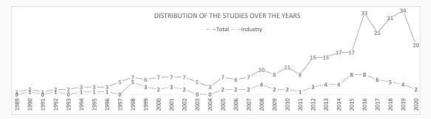


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In fact, formal methods in railways is a thriving research field with strong industrial ties: 143 studies (44% of a total of 328) published solely in the last 5 years, while 79 studies (24%) involved industry



[CSUR23] A. Ferrari & M.H. ter Beek, Formal Methods in Railways: A Systematic Mapping Study. *ACM Computing Surveys* (2023)

# Formal methods in railways



#### Systematic mapping study of formal methods for railway signalling systems

K. Petersen, S. Vakkalanka & L. Kuzniarz, Guidelines for conducting systematic mapping studies in software engineering: An update. *Information and Software Technology* (2015)



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RQ1 How is research demographically and empirically characterized in the field of applications of formal methods in the railway domain?
RQ2 What formal methods are used in the railway domain?
RQ2.1 What is the degree of formality?
RQ2.2 What formal techniques are used?
RQ2.3 Which specification languages?
RQ2.4 Which tools?

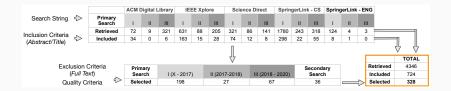
**RQ3** In which way are formal methods applied to railway system development?

**RQ-I** What are the characteristics of the studies reporting *industrial applications?* 

**RQ-T** : What are the emerging trends of the last 5 years?

#### Formal methods in railways: search process







"formal" OR "model check\*" OR "model based" OR "model driven" OR "theorem prov\*" OR "static analysis"

#### AND

"railway\*" OR "CBTC" OR "ERTMS" OR "ETCS" OR "interlocking" OR "automatic train" OR "train control" OR "metro" OR "CENELEC"



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Formal methods require formal models with a precise semantics

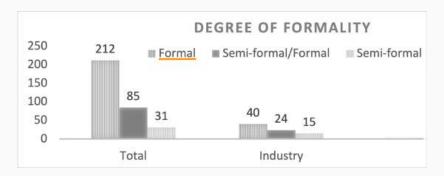


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#### Model Checking

- Automatically check whether a formal model satisfies a temporal logic property (LTL, CTL) and provide a counterexample if it does not
- Exhaustive, but suffers from the state space explosion problem
- BLAST, CADP, JPF, mCRL2, PRISM, (Nu)SMV, SPIN, UMC, UPPAAL, ...



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#### Probabilistic Model Checking (PMC)

- Model check whether a stochastic model satisfies a temporal logic property (PCTL, CSL) with a probability greater than a set threshold
- Model uncertainty/performance; do quantitative analysis (QoS, ...)
- CADP, LiQuor, MRMC, PARAM, PRISM, STORM, UPPAAL PRO, ...



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#### Statistical Model Checking (SMC)

- Simulation-based technique to statistically approximate (P)MC
- Highly parallelisable and automatable; tunable preciseness via CI
- PLASMA, PRISM, UPPAAL SMC, (P)VeStA, MultiVeStA, QFLan, ...



SMC: run a controlled number N of (probabilistically distributed) simulations of a system model to obtain a statistical evaluation p' of some formula  $\varphi$  such that

- $\Pr(|p'-p| \le \epsilon) \ge 1-\alpha$
- *N* only depends on  $\alpha$  (precision, sub-linear) and  $\epsilon$  (confidence, quadratic)



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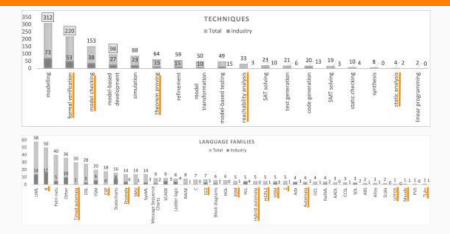
#### UPPAAL SMC:

- frequently used to specify and analyse railway systems, modelled as Stochastic Hybrid Automata (SHA), implementing SMC
- graphical interactive simulations, also basic CTL model checking

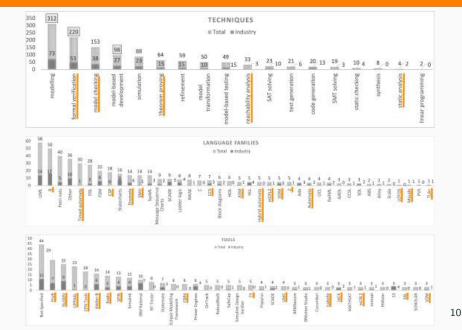
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-ZEWL

#### Indeed, at the basis!





#### Results





#### Take-away messages

- Later development phases, in particular testing, implementation and validation, are currently not sufficiently addressed
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#### Take-away messages

- Later development phases, in particular testing, implementation and validation, are currently not sufficiently addressed
- Formal methods are typically applied on abstract, highlevel models, and source code is only marginally considered
- + UML is typically used for high-level models, which are then translated into input languages of the formal methods tools
- + Almost all core railway development phases can be addressed by formal methods, in line with CENELEC recommendation

# Formal tools in railways



Systematic evaluation of formal tools for system design in railways

B. Kitchenham, S. Linkman & D. Law, DESMET: A methodology for evaluating software engineering methods and tools. *Computing & Control Engineering Journal* (1997)



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RQ1 Which are the features to consider for evaluating formal tools?RQ2 How do different tools compare with respect to these features?RQ3 How do different tools compare with respect to their usability?

[TSE22] A. Ferrari, F. Mazzanti, D. Basile & M.H. ter Beek, Systematic Evaluation and Usability Analysis of Formal Methods Tools for Railway Signaling System Design. *IEEE Transactions on Software Engineering* (2022)

[ICSE20] A. Ferrari, F. Mazzanti, D. Basile, M.H. ter Beek & A. Fantechi, Comparing Formal Tools for System Design: a Judgment Study @ ICSE'20



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Selected tools? Top ranked ones in a survey among railway practitioners [FM19] M.H. ter Beek et al., Adopting Formal Methods in an Industrial Setting @ FM'19

### Characteristics and expertise of study participants



ID	Role in Study	Milieu	Main Function	Age	Sex	Years of Experience in		
						Formal	Railway	FM in
						Methods (FM)	Industry	Railways
1	assessor	academic	workpackage leader	39	М	> 13	3	13
2	assessor	academic	tool developer	62	М	> 20	0	9
3	assessor	academic	researcher	36	М	> 6	0	4
4	expert	academic	group leader	48	М	> 15	0	9
5	expert	academic	project leader	66	F	> 30	0	> 25
6	expert	academic	professor	65	М	> 30	0	> 25
7	expert	industry	system engineer	NA	М	0	> 10	0
8	expert	industry	system engineer	52	М	0	> 10	0
9	expert	industry	system engineer	48	М	0	> 10	0
10	expert	industry	software developer	43	Μ	0	> 10	0
11	expert	industry	product manager	NA	М	0	> 10	0
12	expert	industry	system engineer	48	М	0	> 10	0
13	expert	industry	innovation engineer	NA	М	0	> 10	0
14	expert	industry	software developer	45	М	0	> 10	0
15	expert	industry	innovation engineer	NA	F	0	3 to 10	0



#### Workshop with assessors, academic and industry experts: 33 features



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The assessors produced an evaluation sheet for each tool based on:

- 1 install and run the tool
- 2 consult the tool's website for official documentation
- 3 search for additional documentation
- 4 consult the papers on formal methods and railways from the literature review to check for the tool's application in railways
- 5 perform tool trials to confirm claims from the documentation, and assign value to those features requiring hands-on activity
- 6 report the evaluation, with notes and links to documentation



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Revision during face-to-face meetings to align visions and balance judgements, and reviewed externally as part of a project deliverable

## **Feature evaluation table**



Catagory	Name	SPIN	Simulink	пиХту	ProB	Atelier B	UPPAAL	FOR4	<b>CPN Tools</b>	CADP	mCRL2	SAL	TLA+	UMC
Development	Specification / Modeling	TEXT	GRAPH	TEXTIN	TEXT	TEXT	GRAPH	TEXTIN	GRAPH	TEXTIM	TEXT	TEXTIM	TEAT	TEXT
Functionalities	Code Generation	ND	YES	ND	NO	YES	ND	NO	NO	YES	NO	NO	ND	NO
	Documentation / Report Generation	PARTIAL	YES	ND	PARTIAL	PARTIAL	PARTIAL	PARTIAL	NO	PARTIAL	PARTIAL	NO	NO	PARTIAL
	Requirements Traceability	NO	YES	NO	NO	NO	NO	NO	ND	NO	NO	NO	NO	NO
	Project Management	NO	YES	NO	YES	YES	NO	NO	NO	NO	NO	NO	NO	NO
Verification	Simulation	TEXT	GRAPH	TEXT	MIX	NO	GRAPH	TEXT	GRAPH	TEXT	TEXT	TEXT	NO	TEXT
Functionalities	Formal Verification	MC-L	NC-O	MC-L.MC-B	MC-L,MC-8,RF	TP	MC-L,RF	RF	MC-B	MC-8,RF	NC-B.RF	NC-L,TP	MC-L,TP	NC-B
	Large-scale Verification Technique	FLY.POR.PAR	BINC	BINC.SYM	SCT	SCT	SMC,SYM	COM, POR	BMC	COM,PAR	COM	PAR.SCT	SYM,SCT	FLY
	Nodel-based Testing	NO	YES	NO	YES	NO	YES	NO	ND	YES-	NO	YES	ND	NO
Language	Non-determinism	INT	EXT	INT,EXT	INT,EXT	INT,EXT	INT,EXT	INT, EXT	INT	INT,EXT	INT.EXT	INT,EXT	INT	INT
	Concurrency	ASYNCH	NO	SYNCH	NO	ND	SYNCH	ASYNCH	ASYNCH	ASYNCH	ASYNCH	A/SYNCH	ASYNCH	A/SYNCH
	Timing Aspects	NO	YES	YES	NO	NO	YES	YES	YES	ND	YES	YES	NO	NO
	Stochastic or Probabilistic Aspects	NO	NO	NO	NO	ND	YES	NO	ND	NO	YES	NO	ND	NO
	Nodularity of the Language	HIGH	HIGH	MEDIUM	LOW	LOW	NEDIUM	HIGH	HIGH	HIGH	HIGH	NEDIUM	MEDIUM	HIGH
	Supported Data Structures	BASIC	COMPLEX	COMPLEX	COMPLEX	COMPLEX	CONFLEX	COMPLEX	COMPLEX	CONFLEX	COMPLEX	COMPLEX	COMPLEX	COMPLEX
	Float Support	NO	YES	YES	ND	NO	YES	NO	NO	NO	NO	NO	NO	NO
Tool Flexibility	Backward Compatibility	LIKELY	LIKELY	LIKELY	LIKELY	MODERATE	LIKELY	NODERATE	LIKELY	LIKELY	LIKELY	MODERATE	MODERATE	MODERATE
Vice and	Standard Input Format	OPEN	PARTIAL	OPEN	OPEN	OPEN	PARTIAL	OPEN	PARTIAL	STANDARD	OPEN	OPEN	OPEN	STANDARD
	Import / Export vs. Other Tools	HEDIUM	LOW	MEDIUM	HIGH	MEDIUM	LOW	MEDIUM	MEDIUM	HIGH	HIGH	MEDIUM	LOW	MEDIUM
	Nodularity of the Tool	LOW	HIGH	LOW	HIGH	MEDIUM	HIGH	LOW	LOW	HIGH	MEDIUM	LOW	LOW	NEDIUM
	Team Support	NO	ND	ND	NO	YES	NO	NO	NO	ND	NO	NO	NO	ND
Maturity.	Industrial Diffusion	HIGH	HIGH	HIGH	HIGH	HIGH	HIGH	MEDIUM	NEDIUM	MEDIUM	MEDIUM	LOW	MEDIUN	LOW
	Stage of Development	MATURE	MATURE	MATURE	MATURE	MATURE	MATURE	MATURE	MATURE	MATURE	MATURE	MATURE	MATURE	PROTOTYPE
Unitality	Availability of Customer Support	PARTIAL	YES	PARTIAL	YES	YES	YES	PARTIAL	PARTIAL	PARTIAL	PARTIAL	PARTIAL	PARTIAL	PARTIAL
10000	Graphical User Interface	LINITED	YES	ND	PARTIAL	PARTIAL	YES	LIMITED	PARTIAL	LIMITED	PARTIAL	NO	LINITED	PARTIAL
	Nathematical Background	NEDIUM	BASIC	MEDIUM	MEDIUM	ADVANCED	NEDIUM	ADVANCED	MEDIUM	ADVANCED	ADVANCED	ADVANCED	ADVANCED	NEDIUM
	Quality of Documentation	6000	EXCELLENT	0000	GOOD	EXCELLENT	0000	EXCELLENT	6000	G000	G000	0000	0000	LINITED
Company Constraints	Cost	FREE	PAY	MIX	FREE	FREE	MIX	MIX	FREE	MIX	FREE	FREE	FREE	FREE
	Supported Platforms	ALL	ALL	ALL	ALL	ALL	ALL	ALL	Windows	ALL	ALL	ALL	ALL	ALL
	Complexity of License Management	EASY	ADEQUATE	EASY	EASY	EASY	HODERATE	NODERATE	EASY	NCOERATE	EASY	EASY	EASY	EASY
	Easy to install	YES	YES	YES	YES	YES	YES	YES	YES	PARTIAL	YES	YES	YES	YES
Rabara-specific	CENELEC Certification	NO	PARTIAL	ND	NO	NO	ND	NO	ND	NO	NO	NO	ND	NO
Criteria	Integration in the CENELEC Process	MEDIUM	YES	MEDIUN	YES	YES	NEDIUM	MEDIUM	MEDIUM	MEDIUM	LOW	LOW	LOW	MEDIUM
	1.	SPIN	Gimuliate	nuXmy	ProB	Atelier B	UPPAAL	FDR4	CPN Tools	CADP	mCRL2	SAL	TLA+	UMC

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	Large-scale Verification Technique	FLY,POR,PAR	BMC	BINC, SYN	SCT	SCT	SMC,SYM	COM, POR	BMC	COM, PAR	COM	PAR,SCT	SYM,SCT	FLY
	Nodel-based Testing	NO	YES	NO	YES	NO	YES	NO	NO	YES	NO	YES	ND	ND
Language	Non-determinism	INT	EXT	INT,EXT	INT,EXT	INT,EXT	INT,EXT	INT, EXT	INT	INT,EXT	INT.EXT	INT,EXT	INT	INT
Expressiveness	Concumancy	ASYNCH	ND	SYNCH	ND	ND	SYNCH	ASYNCH	ASYNCH	ASYNCH	ASYNCH	A/SYNCH	ASYNCH	A/SYNCH
	Timing Aspects	NO	YES	YES	ND	NO	YES	YES	YES	ND	YES	YES	NO	NO
	Stochastic or Probabilistic Aspects	NO	NO	NO	ND	ND	YES	NO	ND	NO	YES	NO	ND	NO
	Modularity of the Language	HIGH	HIGH	NEDIUM	LOW	LOW	NEDIUM	HIGH	HIGH	HIGH	HIGH	NEDIUM	MEDIUN	HIGH
	Supported Data Structures	BASIC	COMPLEX	COMPLEX	COMPLEX	COMPLEX	COMPLEX	COMPLEX	COMPLEX	CONFLEX	COMPLEX	COMPLEX	COMPLEX	COMPLEX
	Float Support	NO	YES	YES	ND	NO	YES	ND	NO	NO	NO	NO	NO	NO
Tool Flexibility	Backward Compatibility	LIKELY	LIKELY	LIKELY	LIKELY	MODERATE	LIKELY	NODERATE	LIKELY	LIKELY	LIKELY	MODERATE	MODERATE	MODERATE
	Standard Input Format	OPEN	PARTIAL	OPEN	OPEN	OPEN	PARTIAL	OPEN	PARTIAL	STANDARD	OPEN	OPEN	OPEN	STANDARD
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	Nodularity of the Tool	LOW	HIGH	LOW	HIGH	MEDIUM	HIGH	LOW	LOW	HIGH	MEDIUM	LOW	LOW	NEDIUM
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Usability	Availability of Customer Support	PARTIAL	YES	PARTIAL	YES	YES	YES	PARTIAL	PARTIAL	PARTIAL	PARTIAL	PARTIAL	PARTIAL	PARTIAL
	Graphical User Interface	LINITED	YES	ND	PARTIAL	PARTIAL	YES	LIMITED	PARTIAL	LIMITED	PARTIAL	NO	LIWITED	PARTIAL
	Nathenatical Background	NEDIUM	BASIC	MEDIUM	MEDIUM	ADVANCED	NEDIUM	ADVANCED	MEDIUM	ADVANCED	ADVANCED	ADVANCED	ADVANCED	MEDIUM
	Quality of Documentation	GOOD	EXCELLENT	0000	GOOD	EXCELLENT	0000	EXCELLENT	6000	GOOD	G000	0000	0000	LIMITED
Company Constraints	Cost	FREE	PAY	MIX	FREE	FREE	MUX	MIX	FREE	MIX	FREE	FREE	FREE	FREE
	Supported Platforms	ALL	ALL	ALL	ALL	ALL	ALL	ALL	Windows	ALL	ALL	ALL	ALL	ALL
	Complexity of License Mesagement	EASY	ADEQUATE	EASY	EASY	EASY	MODERATE	NODERATE	EASY	NODERATE	EASY	EASY	EASY	EASY
	Easy to Install	YES	YES	YES	YES	YES	YES	YES	YES	PARTIAL	YES	YES	YES	YES
Ralway-specific	CENELEC Cartification	NO	PARTIAL	ND	NO	NO	ND	NO	ND	NO	NO	NO	ND	NO
Criteria	Integration in the CENELEC Process	MEDIUM	YES	MEDIUN	YES	YES	MEDIUM	MEDIUM	MEDIUM	MEDIUM	LOW	LOW	LOW	MEDIUM
		SPIN	Simulink	nuXmv	ProB	Atelier B	UPPAAL	FDR4	CPN Tools	CADP	mCRL2	SAL	TLA+	UMC

e.g. ProB and Atelier-B both stand out for project management and score well on tool flexibility, maturity and usability; TLA+ much less  $^{\rm 15}$ 

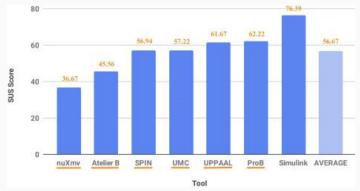


Tool demos presented in meeting with experts: structure, opening, navigating and describing the predeveloped moving block model, followed by guided simulation and formal verification session; after each tool presentation, the experts filled a usability questionnaire

# System Usability Scale (SUS) evaluation



Tool demos presented in meeting with experts: structure, opening, navigating and describing the predeveloped moving block model, followed by guided simulation and formal verification session; after each tool presentation, the experts filled a usability questionnaire



J. Brooke,

SUS: A 'quick and dirty' usability scale. Usability Evaluation in Industry (1996) SUS: A retrospective. *Journal of Usability Studies* (2013)

#### Results



#### Take-away messages

- Many of the formal tools lack support for development features and process-integration aspects
- Most of the formal tools are independent ecosystems, with unique, non-standard languages and specialised verification capabilities

#### Results



#### Take-away messages

- Many of the formal tools lack support for development features and process-integration aspects
- Most of the formal tools are independent ecosystems, with unique, non-standard languages and specialised verification capabilities
- + Formal tools are mature, as highly desired by the railway industry [iFM18,FM19]
- Most usability aspects appear to be low in principle, but, when the formal tools are assessed by railway practitioners, usability is considered acceptable

[iFM18] D. Basile, M.H. ter Beek, et al., On the Industrial Uptake of Formal Methods in the Railway Domain: A Survey with Stakeholders @ iFM'18

# Formal methods & tools in railways: Success stories



#### Using B

- B Automatic Train Protection (ATP) system of Paris RER Line A
- B Subway Speed Control System (SSCS) of the Calcutta subway
- B Paris Metro Line 14 and its derivatives, like NY Canarsie line 1
- B Alstom's U400 system (in use in  $\pm$  100 metro lines worldwide)
- B Driverless Paris–Roissy Airport shuttle



#### Using B, Simulink/Stateflow, NuSMV, SPIN, AADL, UPPAAL, ...

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- Metro control system of Rio de Janeiro
- Movement Authority (MA) scenario of Chinese Train Control System Level 3 (CTCS-3)



• Autonomous Positioning System (APS) of Florence tramways



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ERTMS/ETCS standard, e.g., hybrid ERTMS/ETCS Level  $3 \rightarrow ABZ$  2018 case study, M. Bartholomeus, B. Luttik & T.A.C. Willemse, Modelling and Analysing ERTMS Hybrid Level 3 with the mCRL2 Toolset @ FMICS'18



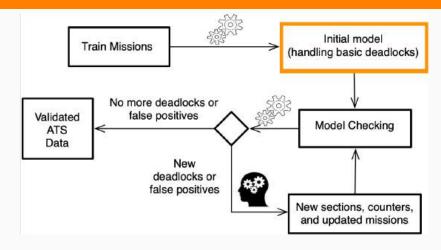
A taste of FMT's railway research in projects with industrial partners over the last 10 years:

- Deadlock avoidance in train scheduling
- Next generation railway signalling systems
- Synthesis of autonomous driving strategies
- Smart railway systems & stations of the future

Success story 1

# Automatic Train Supervision (ATS): deadlocks?





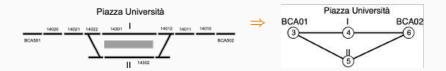
[NFM14] F. Mazzanti, G.O. Spagnolo & A. Ferrari, Designing a Deadlock-Free Train Scheduler: A Model Checking Approach @ NFM14

## Model: choose appropriate level of abstraction!



#### Track circuit:

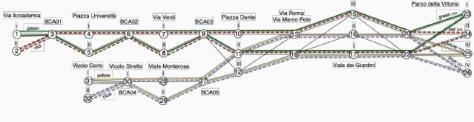
Itinerary:



## Model: case study



Metro layout with 4 lines and 8 trains providing circular services:









[1,3,4,6,7,9,10,13,15,20,23,22,17,18,11,9,8,6,5,3,1] [2,3,4,6,7,9,10,13,15,20,24,22,17,18,11,9,8,6,5,3,2]



[31,30,28,27,11,13,15,20,25,22,17,18,12,27,29,30,31]





[32,30,28,27,11,13,15,20,26,22,17,18,12,27,29,30,32]



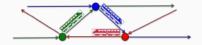
# Model: identify deadlocks



#### Linear deadlock:

Circular deadlock:

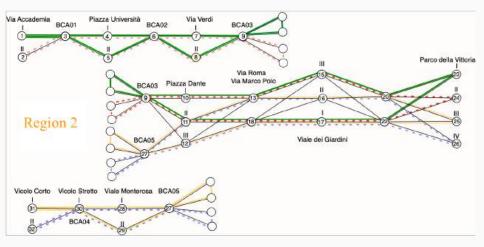




## Divide et impera

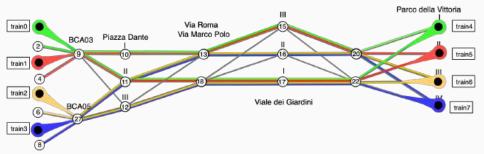


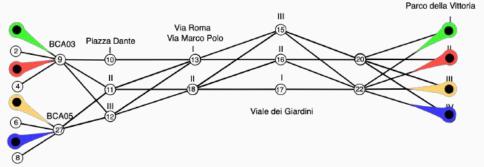
#### Region 1

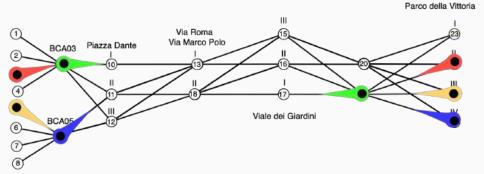


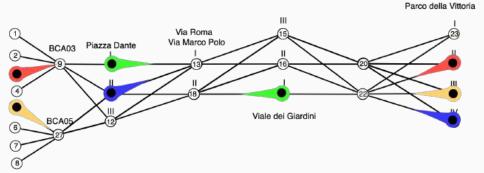
Region 3

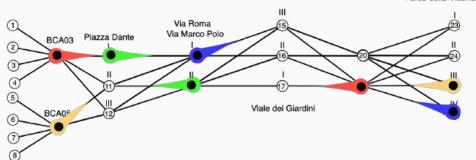




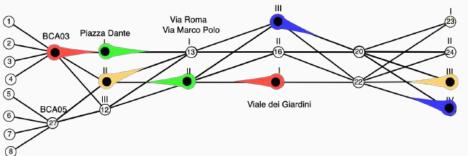




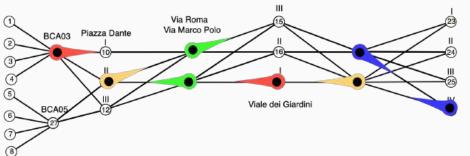




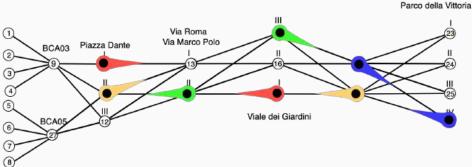
Parco della Vittoria



Parco della Vittoria

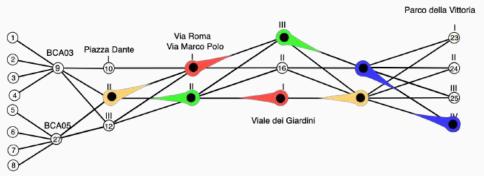


Parco della Vittoria





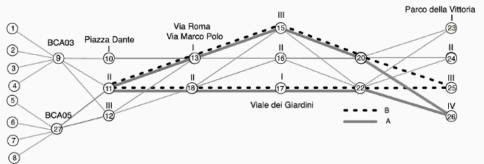
## ... I said without deadlocks!





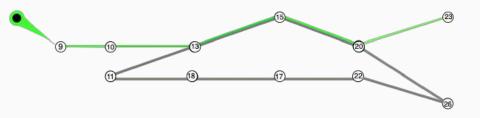
# **Critical sections**





#### **Critical sections: control access**





#### Section A

Counter for section A = RA(init == 1)

Mission  $T_0 = [1, 9, 10, 13, 15, 20, 23]$ 

 $T_0$  operations on RA = [0, 0, 0, +1, 0, -1, 0]

Constraints for section  $A = RA \le 7$ 

#### Results



#### Different tools require different models! ( $\Rightarrow$ different performance)

#	ΤοοΙ	Description	Performance	
1	CADP	Parallel without shared memory $/$ Sequential	29s	
2	CPN Tools	Parallel without shared memory	N/A	
3	FDR4	Parallel without shared memory / Sequential	15s-20m	
4	mCRL2	Parallel without shared memory / Sequential	2m-19m	
5	ProB	Sequential	32m	
6	NuSMV/nuXmv	Sequential	3s-43s	
7	SPIN	Sequential	13s-47s	
8	TLA+	Sequential	3m	
9	UMC	Sequential	38s-86s	
10	UPPAAL	Parallel with shared memory / Sequential	16s	

Note: many modelling languages are textual (e.g., process algebras)

[MARS18] F. Mazzanti & A. Ferrari, Ten Diverse Formal Models for a CBTC Automatic Train Supervision System @ MARS'18

Success story 2

# Next generation railway signalling systems



Current ERTMS / ETCS signalling systems max. level 2:

- fixed blocks: based on line's speed limit, train's speed/braking, ..., thus faster trains imply longer blocks imply lower track occupancy
- trackside equipment for train positioning, with costly maintenance

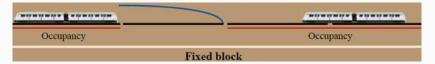
Occupancy		Occupancy
	Fixed block	

# Next generation railway signalling systems



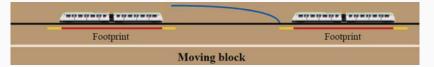
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Next generation railway signalling systems from level 3:

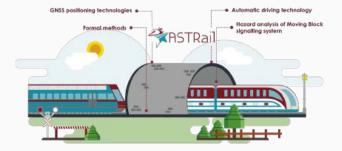
- moving blocks: safe zone based on rear position of train ahead, thus reducing trains' headways, in principle to braking distance
- onboard odometry for train positioning (no trackside equipment)



# Safety analysis of moving block signalling system



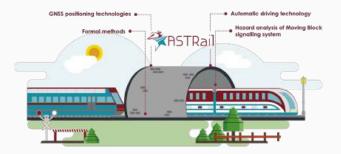
Challenge: effective, precise moving block signalling systems by GNSSbased satellite positioning, leveraging on an integrated solution for signal outages (e.g. tunnels) and multipath interference in dense urban areas





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Aim: analyse suitability of formal methods in transitioning to the next generation of ERTMS/ETCS railway signalling systems, with satellite-based positioning, moving block distancing, and automatic driving





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[STTT22] D. Basile, M.H. ter Beek, A. Ferrari & A. Legay, Exploring the ERTMS/ ETCS full moving block specification: An experience with formal methods. *International Journal on Software Tools for Technology Transfer* (2022)

[FORTE20] D. Basile, M.H. ter Beek & A. Legay, Strategy Synthesis for Autonomous Driving in a Moving Block Railway System with Uppaal Stratego @ FORTE'20

[FMICS19] D. Basile, M.H. ter Beek, A. Ferrari & A. Legay, Modelling and Analysing ERTMS L3 Moving Block Railway Signalling with Simulink and UPPAAL SMC @ FMICS'19

[ISoLA18] D. Basile, M.H. ter Beek & V. Ciancia, Statistical Model Checking of a Moving Block Railway Signalling Scenario with Uppaal SMC @ ISoLA'18

# Main components L3 moving block signalling system



**OBU** train's onboard unit measures the train's current speed and verifies the train's integrity

LU train's localisation unit uses a GNSS-based positioning system to determine the train's location

# Main components L3 moving block signalling system



**OBU** train's onboard unit measures the train's current speed and verifies the train's integrity

- LU train's localisation unit uses a GNSS-based positioning system to determine the train's location
- **RBC** wayside radio block centre communicates continuously with OBU and LU
  - receives data regarding the train's position and the train's integrity from the train
  - sends speed restrictions, route configurations, and MAs (movement authorities) to the train
  - computes MAs by communicating with neighbouring RBCs and with a Route Management System (RMS) for positions of switches and other trains (head and tail position)

# Main components L3 moving block signalling system



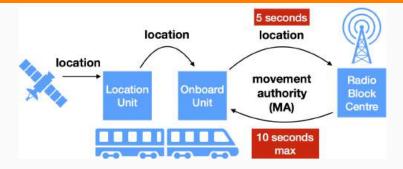
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Model abstraction: assume a train communicates with one RBC, based on seamless handover going from one RBC supervision area to the next 29

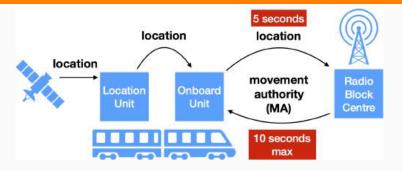
#### Moving block system architecture





# From semi-formal models to formal models





Input: Real-Time UML (RT UML) and Simulink models obtained from/ upon requirements elicitation and refinement with industrial partners

#### Output: UPPAAL SMC model

- capable of natively accommodating both real-time and probabilistic aspects
- $\pm$  UML state machine diagrams, easing understanding by industrial partners

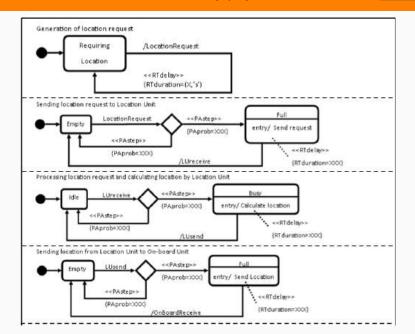
# Model transformation 1: RT UML $\rightarrow$ UPPAAL



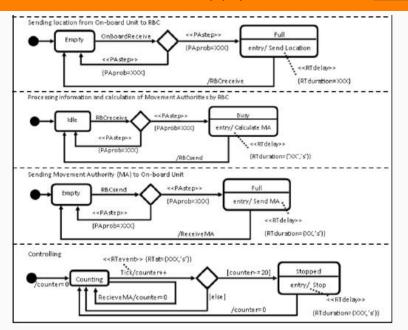
#### From RT UML state machine diagrams to stochastic timed automata

- each parallel region of RT UML model translated into a separate automaton
- (pseudo) states and (probabilistic) transitions are in 1-1 correspondence
- failure probabilities initially set to placeholder value 10<sup>-5</sup> (industry tuning)
- guards and triggers modelled as input and output broadcast channels, thus:
  - synchronous communication, discarding messages if receiver not ready to receive
  - fresh MA sent by RBC to OBU will supersede older MA if latter was not yet received
- straightforward, except some time-related modelling choices cleared with partners
  - timed events RTat of stereotype <<RTevent>>, used to trigger transitions based on event's timing information, modelled as invariant conditions and clock guards, forcing transitions to be executed when the precise moment in time is reached
  - probabilistic delayed events RTduration of stereotype <<RTdelay>>, used to add durations to actions/transitions, modelled as probabilistic delays: when an action/ transition is enabled, the time at which it is fired is probabilistically distributed
- failure probabilities and rates of probabilistic distributions based on industry input

# RT UML models from industry (1/2)

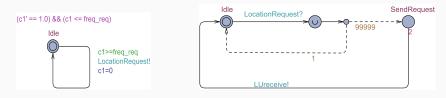


# RT UML models from industry (2/2)



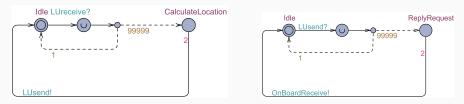


# UPPAAL model of moving block signalling scenario (1/2)



Generate location request

Send location request

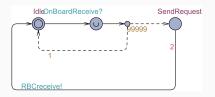


Calculate location

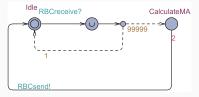
Send location

Industrial partners: freq\_req = 5 sec., initial value clock c1 is freq\_req 34

# UPPAAL model of moving block signalling scenario (2/2)



Send MA request



Calculate MA



Send MA

Control MA freshness

Industrial partners: timeout =  $3 \times \text{freq}_{req}$ , initial value clock counter is 5 35



Goal: evaluate safety level of a moving block signalling system

Procedure: identify and analyse hazards (e.g. GNSS-related errors, communication failures, faulty states)

- risk assessment: probability of occurrence of a hazard and severity of its consequences
- risk qualifying according to CENELEC EN 50126 standard (RAMS: Reliability, Availability, Maintainability and Safety)

Outcome: hazard log

# **TMR**

#### Requirements:

"Communication between RBC and OBU must be safe and continuously supervised, if the connection is lost an alarm must be triggered."

"OBU device must be SIL 4 device. Once OBU receives the alarm [...] it must immediately send an alarm to RBC."

Mitigation: "In case of communication loss enter in safe state mode."

# **FMT**

#### Requirements:

"Communication between RBC and OBU must be safe and continuously supervised, if the connection is lost an alarm must be triggered."

"OBU device must be SIL 4 device. Once OBU receives the alarm [...] it must immediately send an alarm to RBC."

Mitigation: "In case of communication loss enter in safe state mode."

### Safety Related Application Conditions:

"If train position cannot be received within the maximum time limit, the OBU shall generate an alarm and must transit to degraded mode."

"If Train Integrity cannot be confirmed within the maximum time limit, the train shall be stopped."



1 It must always be the case that eventually either a MA is received or the train enters a safe state Stop:

 $A \diamond (\texttt{ReplyMA.ReplyRequest} \parallel \texttt{Controlling.Stop})$ 

UPPAAL SMC reports that this (reachability) property (expressed in CTL) holds



1 It must always be the case that eventually either a MA is received or the train enters a safe state Stop:

 $A \diamond (ReplyMA.ReplyRequest || Controlling.Stop)$ 

UPPAAL SMC reports that this (reachability) property (expressed in CTL) holds

2 Probability that the train enters a safe state Stop upon a timeout:

$$\mathbb{P}_{M}(\diamondsuit_{\leq (\texttt{timeout})} \texttt{Controlling.Stop})$$

UPPAAL SMC reports that this probability is in interval [0,9.99994e-005], with confidence 0.995 and obtained from 59912 runs in  $\pm 5$  min.

# UPPAAL SMC: evaluating the freshness of the MA



Requirements: OBU attempts for three times to compute the train's location and receive the MA

Model: first attempt at time 0, after which OBU attempts again each 5 sec. until timeout at time 15



Requirements: OBU attempts for three times to compute the train's location and receive the MA

Model: first attempt at time 0, after which OBU attempts again each 5 sec. until timeout at time 15

Goal: which of the three attempts has higher probability of success?

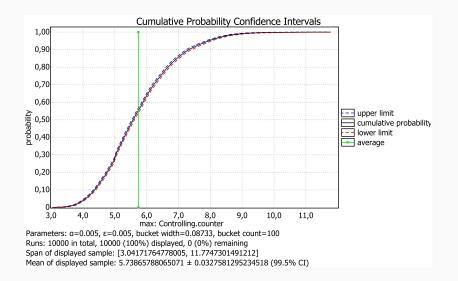
E[< timeout; 10000](max: Controlling.counter)</pre>

This evaluation computes in the interval of time of timeout (i.e. 15 sec.) the average of the maximum value of clock counter, using 10000 runs; Since counter is reset each time a new MA is received, its average value is the average time in which a new MA is received

**Result**: MA messages have a higher probability of being received between the first and the second attempt

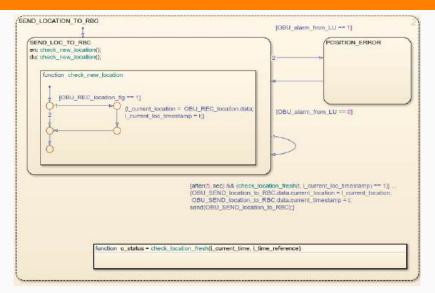
# UPPAAL SMC: evaluating the freshness of the MA





#### Model transformation 2: Simulink $\rightarrow$ UPPAAL

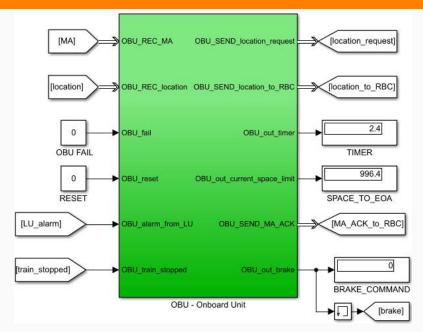




#### Recall: Simulink models obtained upon requirements elicitation and refinement with industrial partners

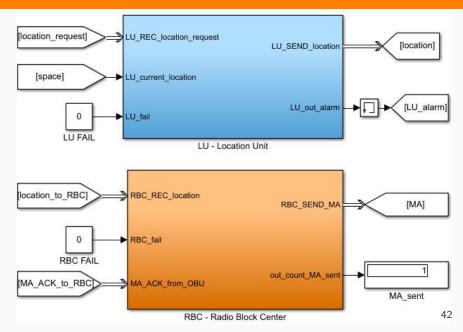
# Architecture of Simulink model (1/2)



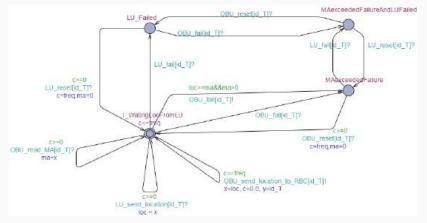


# Architecture of Simulink model (2/2)









TRAIN\_ATO\_T: model train movement (speed, acceleration/deceleration triggered by approaching the limit of the MA, simulating braking curves when reaching failure states) 43



1 Probability that the train's position exceeds the MA (with ma = 1000 m):

 $\Pr[<= 1000](<> OBU_MAIN_SendLocationToRBC.MAexceededFailure)$ 

UPPAAL SMC reports that this probability is in interval [0,0.00998576], with confidence 0.995 and obtained from 597 runs in  $\pm 8$  min.



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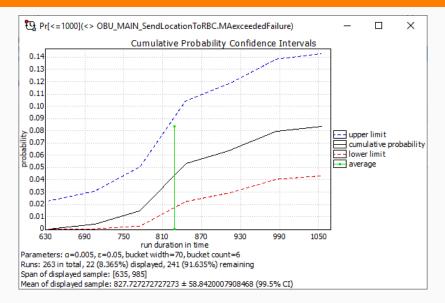
2 Probability that the train's position exceeds the MA (with ma = 500 m):

Pr[<= 1000](<> OBU\_MAIN\_SendLocationToRBC.MAexceededFailure)

UPPAAL SMC reports that this probability is in interval [0.0430205,0.14268], with confidence 0.995 and obtained from 263 runs in  $\pm$  3 min.

# **Analyses with UPPAAL SMC**

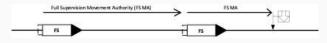




## Full moving block without trackside train detection



#### Model: 3 trains/line + train's MA based on location of train ahead

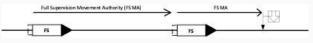


[STTT22] D. Basile, M.H. ter Beek, A. Ferrari & A. Legay, Exploring the ERTMS/ ETCS full moving block specification: An experience with formal methods. *International Journal on Software Tools for Technology Transfer* (2022)



45

Model: 3 trains/line + train's MA based on location of train ahead



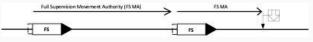
1 Verifying MAs:

State invariant: either the assigned train is ahead of all other trains, or there's no other train whose location is between id\_Train and its MA, and the MA is equal to one such a train ahead

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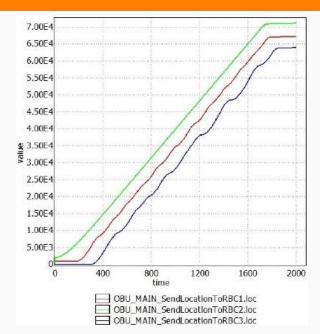
State invariant: either the assigned train is ahead of all other trains, or there's no other train whose location is between id\_Train and its MA, and the MA is equal to one such a train ahead

2 Probability to exceed MA: Pr[<=1000] (<> whofailed==SENDLOC)

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#### Full moving block without trackside train detection





45

# **Results**



Analysis	Results	Lessons learned
Location	The RBC shall always be	Identified communication mismatch due to
freshness	ready to receive new loca-	modelling errors between OBU and RBC,
	tions from the OBU	by comparing the last location received by
		RBC with the last location sent by OBU
Message	All unexpected messages	Errors introduced by refactoring the model
loss	from the RBC/LU/OBU	were identified, by analysing all possible
	can safely be discarded	message exchanges
Verifying	At each attempt, the	Formalising the requirements of 3 trains led
MA	RBC shall compute the	to detect flaws in communication of MA,
	MA the moment it is sent	due to interplay between delays in commu-
	to the OBU	nications and concurrent RBC threads
Exceeding	Under the given assump-	Identified a model with a parameter setup
MA	tions, the model confirms	that confirmed the values from the litera-
	that trains can travel with	ture about headway in high-speed trains
	a headway of 1 minute	46

Success story 3

# Synthesis of autonomous driving strategies



Model transformation 3: UPPAAL SMC  $\rightarrow$  UPPAAL Stratego

• From stochastic timed automata to stochastic priced timed games

[FORTE20] D. Basile, M.H. ter Beek & A. Legay, Strategy Synthesis for Autonomous Driving in a Moving Block Railway System with Uppaal Stratego @ FORTE'20



Model transformation 3: UPPAAL SMC  $\rightarrow$  UPPAAL Stratego

• From stochastic timed automata to stochastic priced timed games

Formal modelling and analysis:

- UPPAAL Stratego: strategy synthesis for timed games (safety) and reinforcement learning of the optimal strategy (reliability)
- While changing the set-up of the parameters, the driving strategy is automatically tuned to retain safety (MA is never exceeded) as well as reliability (minimal expected arrival time)

[FORTE20] D. Basile, M.H. ter Beek & A. Legay, Strategy Synthesis for Autonomous Driving in a Moving Block Railway System with Uppaal Stratego @ FORTE'20



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Model transformation 3: UPPAAL SMC  $\rightarrow$  UPPAAL Stratego

• From stochastic timed automata to stochastic priced timed games

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- While changing the set-up of the parameters, the driving strategy is automatically tuned to retain safety (MA is never exceeded) as well as reliability (minimal expected arrival time)

Results (cf. paper for experiments):

• Experimentation required interacting with the tool developers, resulting in new releases, with patches fixing issues which were discovered through our model!

[FORTE20] D. Basile, M.H. ter Beek & A. Legay, Strategy Synthesis for Autonomous Driving in a Moving Block Railway System with Uppaal Stratego @ FORTE'20 Success story 4



Smart energy consumption: energy efficiency vs. dependability



Smart energy consumption: energy efficiency vs. dependability

• Smart deicing system: railroad switch heaters for correct working of switches in case of ice / snow



Smart energy consumption: energy efficiency vs. dependability

- Smart deicing system: railroad switch heaters for correct working of switches in case of ice / snow
- Smart station lighting: reduce illumination whenever (time) and wherever (space) possible, guaranteeing legal minimum levels









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[ISoLA20] D. Basile, M.H. ter Beek, F. Di Giandomenico, A. Fantechi, S. Gnesi & G.O. Spagnolo, 30 Years of Simulation-Based Quantitative Analysis Tools: A Comparison Experiment Between Möbius and Uppaal SMC @ ISoLA'20

[FMICS21] M.H. ter Beek, V. Ciancia, D. Latella, M. Massink & G.O. Spagnolo, Spatial Model Checking for Smart Stations: Research Challenges @ FMICS'21



Smart energy consumption: energy efficiency vs. dependability

 Smart deicing system: railroad switch heaters for correct working of switches in case of ice / snow



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Also combined strategy synthesis with spatial model checking to synthesise a strategy for 2 trains to safely traverse a junction area (modelled as a game)

[STTT23] D. Basile, M.H. ter Beek, L. Bussi & V. Ciancia, A toolchain for strategy synthesis with spatial properties. *International Journal on Software Tools for Technology Transfer* (2023)

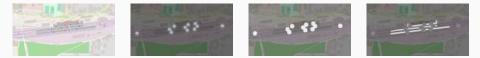


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Smart maintenance: railway specific model learning techniques (with real-time aspects and a degree of uncertainty) to facilitate predictive maintenance by real-time monitoring and simulation (more later)

[REW24] R. Ferdous, G.O. Spagnolo, A. Ferrari, et al., Identifying Maintenance Needs with Machine Learning: A Case Study in Railways @ AIRE@RE'24

### Case study: railroad switch heating system





• Railroad switches: critical system



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- Heating system: tubular flat heaters, induction heating, possibility for energy management
- Cyber-Physical System: sensors for the temperatures
  - weather forecast: *stochastic* (physical)
  - temperature evolution: continuous (physical)
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Contribution: comparison experiment on selected features between two formal modelling and analysis frameworks:

- Stochastic Activity Networks (SAN) and Möbius
- Stochastic Hybrid Automata (SHA) and UPPAAL SMC



Möbius: distributed discrete-event simulator and explicit state-space generators and numerical solution algorithms for Markovian models; analysis of both transient and steady-state reward models



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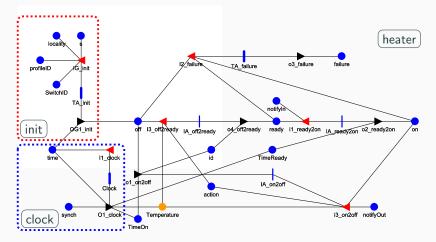
Stochastic Activity Networks: generalisation of stochastic Petri nets

- places & activities: same interpretation as places & transitions in PNs
- input gates control the enabling conditions of an activity and define the change of marking when an activity completes; output gates define the change of marking upon activity completion
- activities can be instantaneous or timed
  - instantaneous activities complete once enabling conditions are satisfied
  - timed activities take time to complete (stochastic distribution of time)
- cases are associated to activities, used to represent probabilistic uncertainty about the action taken upon completion of the activity
- policies of activation/reactivation of activities
- primitives are defined using C++ code



### Möbius: RailRoadSwitchHeater SAN

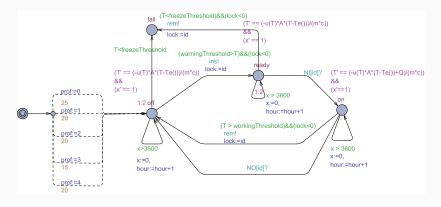




$$mcrac{\partial T}{\partial t} = -uA(T - T_{env}) + \dot{Q}$$

## UPPAAL SMC: RailRoadSwitchHeater SHA

- A coordinator K to manage NH<sub>max</sub> and priorities
- The composed system:  $\mathbb{N} = (\bigotimes_{i \in 1, ..., n} \mathbb{H}_{id}) \otimes \mathbb{K}$
- Array of channels for one-to-one communication



$$mcrac{\partial T}{\partial t} = -uA(T - T_{env}) + \dot{Q}$$



Energy consumption: time (in hours) a generic heater is activated in a specific time interval; by multiplying such measurement for the power consumed (kW/h), one can derive the energy consumption

- Möbius: sum of time spent in specific markings (reward structure)
- UPPAAL:  $E[\le 24; 10000] (max : \sum_{i:id_t} H_i.energy)$



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Cf. papers for results (aligned, find the right parameter setup as best compromise for tradeoff between energy consumption and reliability)





**Modelling features:** the composition of, and interactions between, different models (i.e., *heterogeneous formalisms, replicated models, dynamic process instantiation, communication primitives*) and the ability towards modelling hybrid and stochastic systems (i.e., *delay distributions, hybrid variables*)



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- **Properties specification:** the definition of *measures of interest* and the ability to verify properties of the defined models (i.e., *property verification*)
- **Experiments and presentation of results:** setup and execution of experiments, as well as data collection and plotting the results (i.e., *experiments parameter setup*)

# Comparison



Features	SAN+Möbius	SHA+UPPAAL SMC
Measures of interest	Reward models	MITL formulae
Experiments parameter setup	Batches	Single
Replicated models	Anonymous	Distinguished
Dynamic process instantiation	Not available	Available
Heterogeneous formalisms	Available (SAN, PEPA, etc.)	Not available (SHA)
Communication primitives	Shared places	Channels
Delay distributions	Various distributions	Exponential, uniform
Hybrid variables	No primitive support	ODE solver available
Property verification	Not available	Temporal logics





Possible future improvements for usability

#### Results



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Möbius could provide primitive support for:

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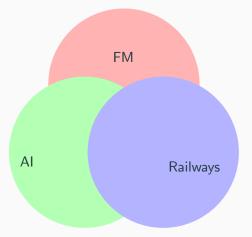
UPPAAL SMC could provide primitive support for:

- batches of experiments with different parameters (well received by K. Larsen @ ISoLA's SMC track)
- other distribution delays (e.g., deterministic time)

**Future challenges** 

#### FM & AI in Railways?





"Every good talk should include a Venn diagram" - Einar (Lima, Peru, 2023)

### Future challenges: AI



## EU Sustainable and Smart Mobility Strategy: Putting European transport on track for the future

https://transport.ec.europa.eu/system/files/2021-04/2021-mobility-strategy-and-action-plan.pdf



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Railways is expected to contribute by improved digitalisation and big data analytics:

R. Tang, L. De Donato, N. Bešinović, F. Flammini, R.M.P. Goverde, Z. Lin, R. Liu, T. Tang, V. Vittorini & Z. Wang, A literature review of Artificial Intelligence applications in railway systems. *Transportation Research Part C: Emerging Technologies* 140 (2022).



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 $\Rightarrow$  AI research in railways still in its early stages, yet major efforts dedicated to the use of AI for innovating rail maintenance policies (not surprising: promises the minimisation of equipment downtime thus reducing the high operational costs characteristic of railways)

### Big data analytics: predictive maintenance



Aim: detect failures before they actually occur



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?! how's the data collected and exploited to implement prognostics:

- **model-based** use the provided input data on a previously defined physical or mathematical model
- **data-driven-based** use a statistical model inferred from the data available at the time of training the prognostics application
- knowledge-based use domain knowledge (e.g., ontologies) or expertise of the system
- **digital twin-based** use a real-time digital representation of the physical system to generate data imitating the real events



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 $\Rightarrow$  development of effective and efficient predictive maintenance solutions for railways is a challenging and emerging research field: 24 papers in the literature for the period 2016–2021

M. Binder, V. Mezhuyev & M. Tschandl, Predictive Maintenance for Railway Domain: A Systematic Literature Review. *IEEE Engineering Management Review* 51, 2 (2023), 120–140.



### Our approach (collaboration with Trenord)



 $\Rightarrow$  Data-driven strategies for on-board equipment, focusing on local commuter trains Traction Control Unit (TCU) and Door CU (DCU)

TCU: adherence to acceleration/deceleration values from regulations



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DCU: related to safety, security and efficiency of railway operations, implying that its failure can lead to operational disruptions/delays that may propagate, thus causing economic loss and bad reputation (DCU responsible for 30% - 60% of total failures in railway vehicles)



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Data-driven as opposed to model-based approaches:

- Require huge amounts of data to correctly infer a statistical model

+ Domain-agnostic nature guarantees instant applicability on the data





[ISoLA22] M. Seisenberger, M.H. ter Beek, X. Fan, A. Ferrari, et al., Safe and Secure Future AI-Driven Railway Technologies: Challenges for Formal Methods in Railway @ ISoLA'22



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• Integrate AI-based systems into the CENELEC standards



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- Integrate AI-based systems into the CENELEC standards
- Equip verification tools with certificate generation in case of a "yes"-answer, to achieve improved trustworthiness (e.g., by *explainability* or *certified model checking*)

Explainability is one of the 10 principles of applicable formal methods

M. Gleirscher, J. van de Pol & J. Woodcock, A manifesto for applicable formal methods. *Software and Systems Modeling* 22, 6 (2023), 1737–1749.

## **Concluding remarks**



semi-formal models popular and suitable to communicate with industry formal models required to apply analysis tools in safety-critical domains (e.g., railways)



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Road to success:

capacity to abstract pick the right tool based on the industry input and requirements at hand



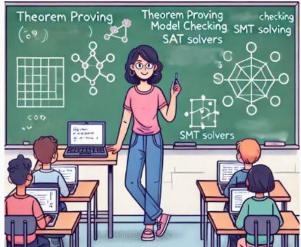
The successful application of formal methods and tools in industries (e.g., in railways) requires to start from the basis  $\rightarrow$  CS education!

### Future of formal methods



The successful application of formal methods and tools in industries (e.g., in railways) requires to start from the basis  $\rightarrow$  CS education!

 M. ter Beek, M. Broy & B. Dongol, The Role of Formal Methods in Computer Science Education. ACM Inroads 15, 4 (2024), 58–66. https://doi.org/10.1145/3702231



### Future of formal methods



#### The successful application of formal methods and tools in industries

(e.g., in railways) requires to start from the basis  $\rightarrow$  CS education!

- M. ter Beek, M. Broy & B. Dongol, The Role of Formal Methods in Computer Science Education. ACM Inroads 15, 4 (2024), 58–66. https://doi.org/10.1145/3702231
- B. Dongol, C. Dubois, S. Hallerstede, E. Hehner, C. Morgan, P. Müller, L. Ribeiro, A. Silva, G. Smith & E. de Vink, On Formal Methods Thinking in Computer Science Education. Formal Aspects of Computing 37, 1 (2025), 8:1-8:23. https://doi.org/10.1145/3670419
- ⇒ formal methods thinking provides the necessary rigour in reasoning on correctness and the fundamental skill of abstraction
  - 3 M. Broy, A. Brucker, A. Fantechi, M. Gleirscher, K. Havelund, M.A. Kuppe, A. Mendes, A. Platzer, J. Ringer & A. Sullivan, Does Every Computer Scientist Need to Know Formal Methods? *Formal Aspects of Computing* 37, 1 (2025), 6:1–6:17. https://doi.org/10.1145/3670795
- ⇒ knowing formal methods provides the indispensable solid foundation that forms the backbone of CS practice
  - 4 M.H. ter Beek, R. Chapman, R. Cleaveland, H. Garavel, R. Gu, I. ter Horst, J.J.A. Keiren, T. Lecomte, M. Leuschel, K.Y. Rozier, A. Sampaio, C. Seceleanu, M. Thomas, T.A.C. Willemse & L. Zhang, Formal Methods in Industry. *Formal Aspects of Computing* 37, 1 (2025), 7:1–7:38. https://doi.org/10.1145/3689374
- $\Rightarrow$  increase in using formal methods in industry, not limited to safety-critical domain

## Acknowledgements

### Thanks to my co-authors of the work presented:













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# Publicity



I would be glad to welcome some of you in Pisa, 26-28 November



Paper deadline: June 13<sup>th</sup>

Chairs: Maurice ter Beek, CNR–ISTI, Pisa, Italy Simon Collart Dutilleul, Université Gustave Eiffel, France Thierry Lecomte, CLEARSY, France



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