

# BCS - FACS

85.12.17

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Lecture 2

Graph + Rapid Prototyping

~~\_\_\_\_\_~~  
Mule

M  
M  
I

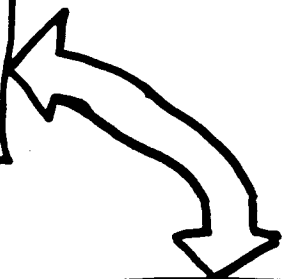
TOOLS

Meta:  
GRAPL

Special:  
Provers  
Checkers

DATA  
Model / Objects

Mule  
Data  
Base



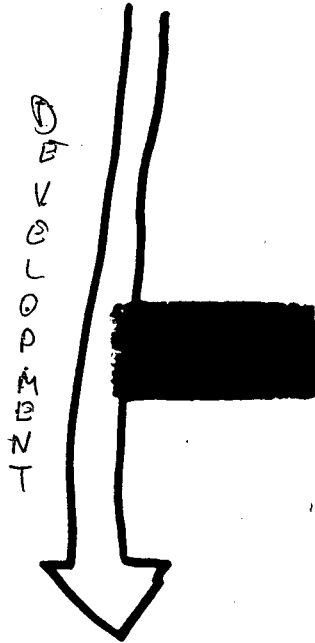
Data objects - MDB

Model

BRM

General

query oriented



Specific Application  
controlled queries

Directed Graphs (+) ...

MDB supports:

- Labelled, ~~directed~~, graphs
  - nodes/arcs labelled by entities
  - nodes may contain values
  - one-one (nodes, entities)
  - many-one (nodes, values)
  - many-one (arcs, entities)

'Node' and ~~Entity~~ are synonymous

- Lists
  - Sets
  - Value maps
- } convenience

• DATA MANIPULATION LANGUAGE  
"GRAPL"

Experimental

VHLL for database (MDB)

Query and Update

- Prolog
- Typol (lexer)

• support [redacted] activities beyond  
the context-free

- type checking
- formula transformation [redacted]
- proof checkers

[redacted]

Q. Why Prolog-like?

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need ...

- logical inferences on MDB
- (subgraph) pattern-match<sup>ing</sup>  
& symbolic manipulations
- ability to handle incomplete  
data (e.g. syntax graph  
with "holes")

[ + not much time to design /  
"re-invent" ]

# Graph ~~\_\_\_\_\_~~ Prolog\_\_\_\_\_

- almost identical  
(" to our surprise ~~\_\_\_\_\_~~)
  - all Graph predicates  
are typed.
  - concept<sup>t</sup> ~~\_\_\_\_\_~~<sup>of</sup> type  
inheritance.
  - several trivial details.
-



Sub-<sup>type</sup>type e.g.

person :: A: Age, N: Name;

student :: person + C: Dept,  
U: Univ

young ( person (?AGE, -) )

<= ?AGE < 21.

?- young ( student(3, -, -, -) ).

yes

# ABSTRACT SYNTAX

Formula

Formula :: TERM: Term;

Term :: EXPR: Expr;

Expr = vari | true | false | <sup>undef</sup> undef  
or | neg | impl | turestile;

vari :: VAR: Id;

true :: ; false :: ; undef :: ;

or :: O1: Expr O2: Expr;

and :: A1:  A2: Expr;

impl :: A: Expr, C: Expr;

neg :: NOT: Expr;

turastile :: LHS: Expr, RHS: Expr;

[REDACTED]

(\* Graph prog. A.S. \*)

check :: T: Term;

copy :: INE: Expr, INB: List,  
OUTE: Expr, OUTB: List;

taut :: E: Expr, L: List;

falsify :: E: Expr, L: List;

eval :: E: Expr, V: Expr;

isin :: I: Id, V: Expr, L: List;

writeVal :: L: List;

Triple :: I: Id, V: Expr, L: List

List :: Triple | nil; nil :: ;

---

  
PropLogic 3:

check (Term (?E)) <=

copy (?E, nil, ?C)

taut (?C, ?B).

ATTACH <sup>check</sup>  TO Term

WITH "Tautology check".

taut (?E, <sup>?B</sup> ~~??~~) <=  
not ( falsify (?E, ?B) ),  
write (<'TAUTOLOGY!'>),  
writeln.

falsify (?E, ?B) <=  
eval (?E, false),  
write (<'False with valuation'>),  
write (~~??~~),  
writeVal (?B).

(\* ditto for eval (?E, undef) \*)

---

$\text{eval}(\text{true}, \text{false})$ .  $\text{eval}(\text{false}, \text{false})$   
 $\text{eval}(\text{undef}, \text{undef})$ .  $\text{eval}(\text{vari}(\text{?v}), \text{?v})$

$\text{eval}(\text{and}(\text{?E1}, -), \text{false}) \Leftarrow$   
 $\text{eval}(\text{?E1}, \text{false})$ .

$\text{eval}(\text{and}(-, \text{?E2}), \text{false}) \Leftarrow$   
 $\text{eval}(\text{?E2}, \text{false})$ .


$\text{eval}(\text{and}(\text{?E1}, -), \text{undef}) \Leftarrow$   
 $\text{eval}(\text{?E1}, \text{undef})$ .

$\text{eval}(\text{and}(-, \text{?E2}), \text{undef}) \Leftarrow$   
 $\text{eval}(\text{?E2}, \text{undef})$ .

$\text{eval}(\text{and}(\text{?E1}, \text{?E2}), \text{true}) \Leftarrow$   
 $\text{eval}(\text{?E1}, \text{true}), \text{eval}(\text{?E2}, \text{true})$

(\* ditto )

eval {  
  or  
  impl  
  turnstile  
  reg





|

copy (var ( ?I ) ?ENV, var (?V), ?ENV)  
←= isin (?I, ?V, ?ENV), !.

copy (var (?I), ?ENV, var (?V),  
Eriple (?I, ?V, ?ENV)

copy (true, ?I, true, ?I).

copy (false, ?I, false, ?I).

copy (undef, ?I, undef, ?I).

copy (and (?E1, ?E2), ?I, and (?C1, ?C2), ?O2)

←= copy (? ?C1, ?O1),

copy (?E2, ?O1, ?C2, ?O2).

(\* ditto for copy of or, impl,  
Eunastrik and neg \*)



isin(?I, triple(?I, ?V, -)).  
isin(?I, ?V, triple(-, -, ?L)) ←  
isin(?I, ?V, ?L).

writeVal ( triple(?I, ?V, ?L) )  
write (< ?I, '=', ?V >),  
writeln,  
writeVal (?L).

writeVal ( )

## Limitations of Current Ver.

- cannot update database.
    - need  $\approx$  assert/retract
  - sub-typing ("inheritance") is ~~also~~ too slow.
    - need ~~assert~~ <sup>Smart</sup> implementation
  - all GrepL output goes to a monitor window (not current)
    - needs fixing.
-

Rapid

Prototyping

of

VDM

Specifications.

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# Why?

- early customer feedback:
  - would this functionality satisfy your requirements?
- helps specifier/designer:
  - is spec. consistent, computable, etc.?

(primitive theorem proving assistance)

# DANGER!

- encourages TEST-CASE approach to correctness

- management reluctant to have staff "wasting time" proving theorems if Spec./Prototype passes all Q.A.'s tests.

Credits:

IDC [redacted] White +

Shirley McAse

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## Goal

The automatic (or, at least, semi-automatic) generation of a prototype implementation from a [redacted] formal spec.

(N.B. not possible for all VDM specs.)

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## PROBLEMS

• post  
 $\exists i \in \mathbb{N}.$

$$\neg \exists j \in \mathbb{N} \cdot j > i$$

(\* "there is a largest value  
in the set of naturals" \*)

•  $\forall i \in \mathbb{N} \cdot \text{even}(i) \vee$   
 $\text{odd}(i)$

univ. quant.  
over infinite set.

## Fundamental Requirement

A person writing an abstract formal spec. in VDM should not have to know about or be concerned about the prototyping process.

⇒ better for prototype generation to fail than spec. is "infected".



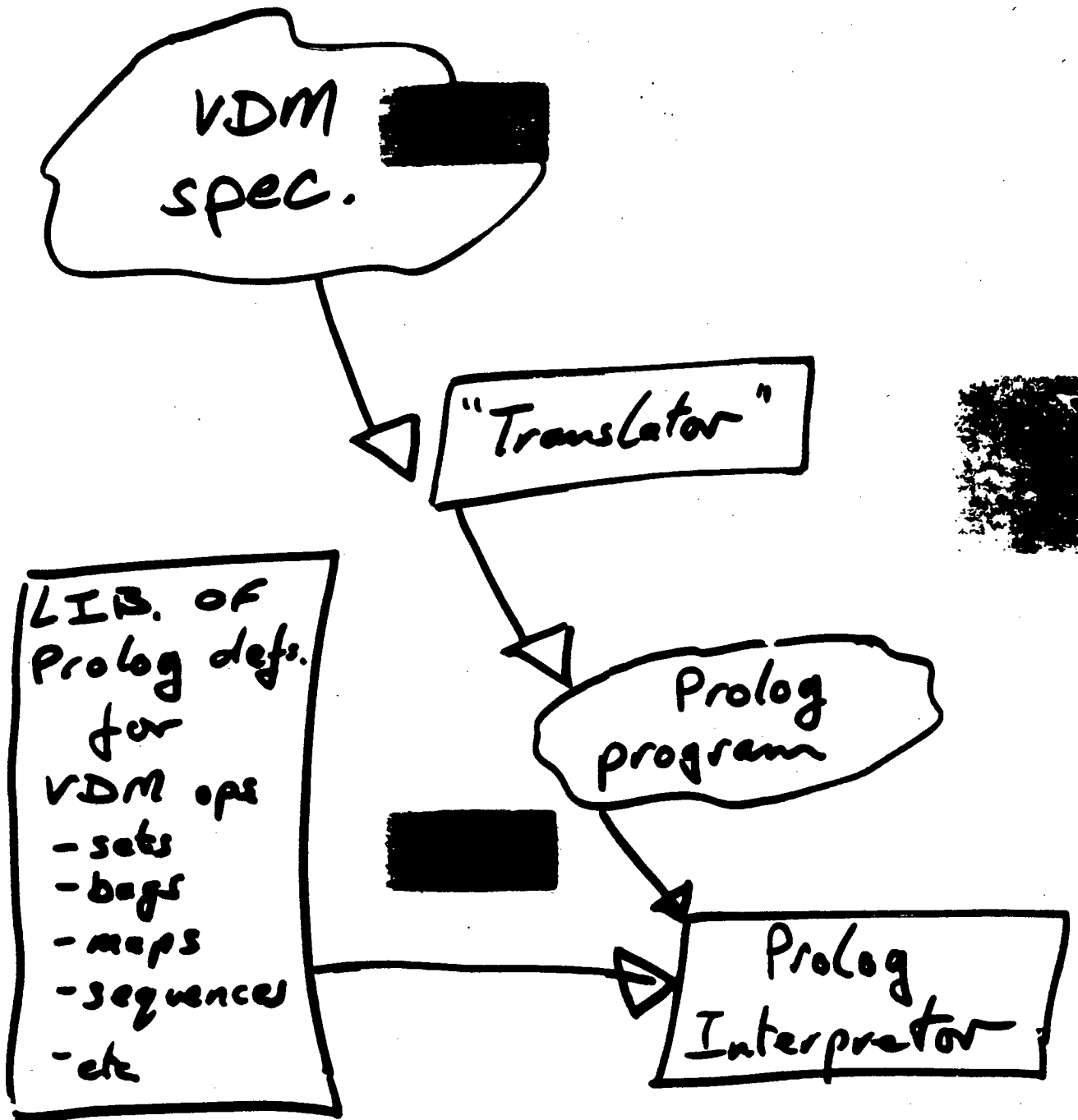
VDM spec.

{ pred. logic  
relational  
(non-deterministic)



Prolog

{ pred. Logic  
(Horn cl.)  
relational  
(non-deterministic)



e.g. <sup>defn:</sup> Implicit  of seq. concat.

VDM:  $\text{concat} : \text{seq of } X + \text{seq of } X \rightarrow \text{seq of } X$

post-concat  $(l_1, l_2, r)$

$\text{len } r = \text{len } l_1 + \text{len } l_2$

$r(1 \dots \text{len } l_1) = l_1 \wedge$

$r(\text{len } l_1 + 1 \dots \text{len } r) = l_2$

Prolog:

$\text{concat}(L_1, L_2, R)$  :-

$\text{len}(L_1, \text{LENL1}), \text{len}(L_2, \text{LENL2}),$

$\text{LENR}$  is  $\text{LENL1} + \text{LENL2}, \text{con}(R, \text{LENR})$

$\text{subl}(R, 1, \text{LENL1}, \text{RF}), \text{RF} = L_1,$

$\text{MID} = \text{LENL1} + 1, \text{subl}(R, \text{MID}, \text{LENR}, \text{RB}),$

$\text{RB} = L_2.$

VDM

sets

sequences

maps

bags

State  
Variables



Prolog

lists (unique)

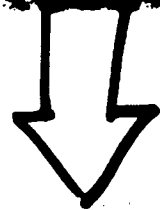
lists

lists of 2-tuples  
(unique 1<sup>st</sup> element)

lists of 2-lists  
("map X to N")

Prolog's  
facts/rules  
Data Base

VDM ~~post~~ conditions



post<sub>OP</sub> ( ... ) :-  
state ( old ),

↑  
translated post-cond  
↓ ( post ( old, .., New )  
~~reflect~~ ( state ( \_ ),  
asserta ( state ( New ) )

e.g. The TEST ~~is~~ <sup>from the</sup> "Equivalence Relation" problem.

VDM: Part = set of set of Pno

TEST (p1, p2: Pno) r: B

ext rd P: Part

post r  $\iff \exists s \in \bar{P}. p1 \in s \wedge p2 \in s$

Prolog:

test (p1, p2) :-

state ~~is~~, member(s, p),

member(p1, s), member(p2, s).

(\* No state change \*)

# The EQUATE op.

VDM

EQUATE ( $p_1, p_2 : P_{no}$ )

ext wr  $P : Part$

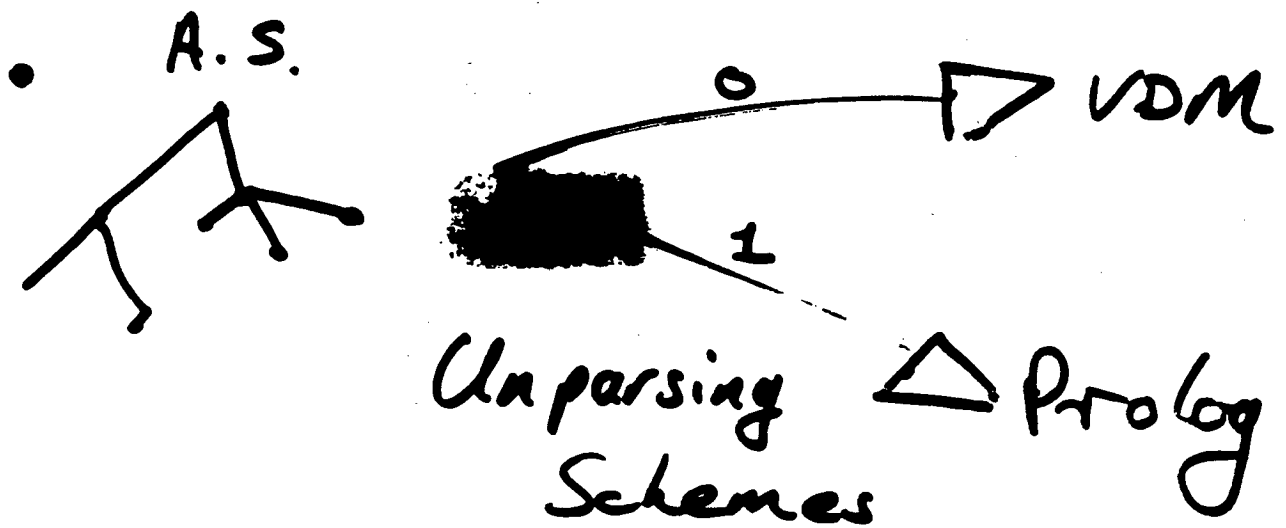
post  $P =$

$\{s \in \tilde{P} \mid p_1 \notin s \wedge p_2 \notin s\}$

$\cup \{ \cup \{s \in \tilde{P} \mid p_1 \in s \vee p_2 \in s\} \}$

# Current Implementation

- Semi-automatic translation, based on a VDM-editor generated via the C.A. C.N.U. ALOEGEN program.





## Future Plans

- Multi-based system
  - fully automatic
  - action routines in  
Graph producing
- General investigation of  
Rapid Prototyping  
within IPSE 2.5 ALvey  
project.

Male :

- single-user, workstation-based,  
"IPSE"

- ICL / Perq Systems PERQ I

- UNIX

- Pascal (UNIX-Portable)

- VDM version

# Major influences:

- Gandalf CMU (USA)
- Mentor INRIA (France)
- PS G Darmstadt (Germany)

+ "Workstation  
concepts

Xerox PARC (USA)

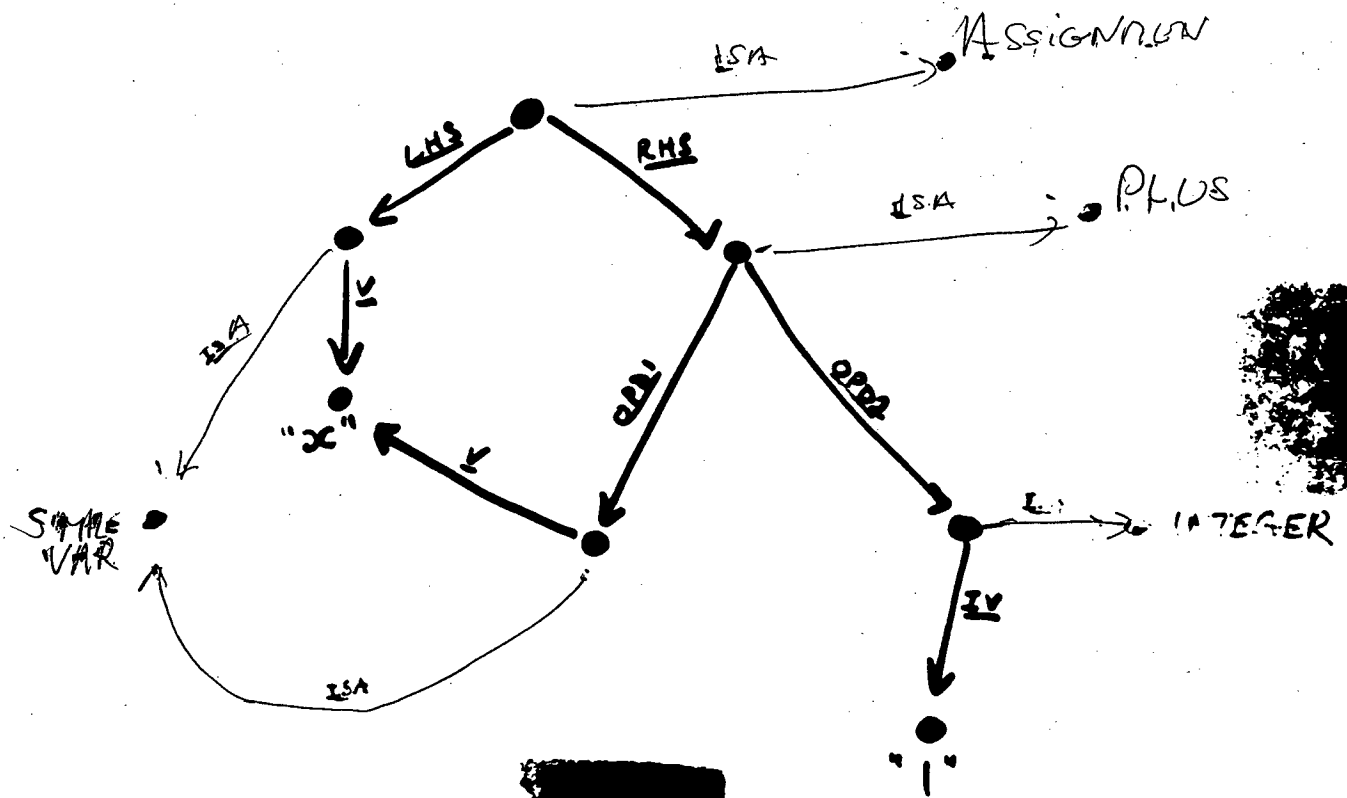
## Related Work

Zs. Farkas, P. Szeredi, and  
E. Santane-Toth,

"LDM - a program specification  
support system."

Proc. 1<sup>st</sup> Int. Conf. on Logic Prog.  
Marseille, 1982.

$x := \text{[REDACTED]}$



Q. How to make graph links?

- MMI pointing device
- "Dynamic syntax" for text I/P
- Explicit action routine in some DBL.

$$E1 \xrightarrow{\text{[REDACTED]}} E3$$

$$E1 \xrightarrow{E2} S = \{F1, \dots, F_n\}$$

$$E1 \xrightarrow{E2} L = \{F1, \dots, F_n\}$$

$$E_v \xrightarrow{\text{[REDACTED]}} VS = \{F1, \dots, F_n\}$$